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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/577,547	05/24/2000	Cornelis G.M. Van Asma	PHn 17,450	5029

24737 7590 01/14/2004

PHILIPS INTELLECTUAL PROPERTY & STANDARDS  
P.O. BOX 3001  
BRIARCLIFF MANOR, NY 10510

EXAMINER

TRAN, TRANG U

ART UNIT	PAPER NUMBER
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2614

DATE MAILED: 01/14/2004

15

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/577,547

Applicant(s)

VAN ASMA, CORNELIS G.M.

Examiner

Trang U. Tran

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 20 October 2003.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1 and 3-10 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1 and 3-10 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. §§ 119 and 120

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All b) ☐ Some \* c) ☐ None of:  
1. ☒ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 13) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.  
a) ☐ The translation of the foreign language provisional application has been received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.

## Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

## DETAILED ACTION

### *Response to Arguments*

1. Applicant's arguments filed Oct. 20, 2003 have been fully considered but they are not persuasive.

In re pages 5-6, applicant argues that Werner fails to teach a digital video processing unit including such a memory manager and processor because the same processor in claim 1 processes input data into output data, transfers the output data to the memory means for output to a display unit, receives the stored output data signal it previously produced from the memory means, and executes a further operation on the stored output data signal while Werner's uses a pair of separate processing engines 24 (decoding engine) and 25 (scaling engine) to perform similar processing as that of the single processor of claim 1.

In response, the examiner respectfully disagrees. As recognized by application that a pair of separate processing engines 24 and 25 perform similar processing as that of the single processor of claim 1. Additionally, Werner also discloses in col. 5, lines 35-45 that "A feature of the invention is that the components of video processing unit 13 are designed so that they can be fabricated with integrated circuits. For example, the processing engines 24 and 25 and the memory manager 23 might be fabricated as **one integrated circuit**. This type of circuit is often referred to as an ASIC (application specific integrated circuit), with the applications being decompression and re-sizing of digital image data. As another example, memory 23 might also be "on-chip", such that the entire video processing unit 13 is a **single integrated circuit**". Since the processing

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engines 24 and 25 are fabricated in one integrated circuit, the integrated circuit would anticipate the claimed processor. Thus, Werner does indeed disclose all the limitations of claim 1.

In re page 6, applicant states that claims 3-10 depend from independent claim 1, and are, therefore, patentable for at least the reasons set forth above.

In response, as discussed above with respect to claim 1, Werner discloses all the limitations of claim 1.

***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Werner, William B. (EP 0 840 522).

In considering claim 1, Werner, William B. discloses all the claimed subject matter, note 1) the claimed processor for processing the input signal to the output data signal is met by the decoding engine 24 or the scaling engine 25 (Fig. 3, col. 5, lines 25-51 and col. 6, lines 27-54 and col. 7, lines 41-57), 2) the claimed memory means for storing the input signal prior to supply to said processor is met by the share memory 33 (Fig. 3, col. 5, line 52 to col. 6, line 26 and col. 7, lines 41-57), and 3) the claimed a memory manager coupled with the processor and the memory means, the memory manager being arranged to transfer the input data signals to the memory means and to

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transfer the stored input data signal from the memory means to the processor, respectively, wherein an output of the processor is coupled to an input of the memory manager, wherein the memory manager is further arranged to transfer the output data signal from the processor to the memory means for storing the output data signal, and to transfer the stored output data signal from the memory means to the display unit and wherein the memory manager is further arranged to transfer the stored data output signal from the memory means to the processor, and said processor is further arranged to execute a further operation on the stored data output signal is met by the memory manager 22 of the video processing unit 13 and the video processing unit 13 which decompresses the data and scales (re-sizes) the resulting image to match the display resolution of SLM 16 (Figs. 1 and 3, col. 5, line 52 to col. 6, line 11 and col. 7, lines 41-57).

In considering claim 3, the claimed wherein said processor and said memory manager are arranged to execute different processes in time multiplex is met by col. 5, lines 35-56.

In considering claim 4, the claimed wherein said processor, said memory manager and the memory means are comprised on a single integrated circuit is met by Fig. 3, col. 7, lines 41-57.

In considering claim 5, the claimed wherein said processor is arranged to resize an image represented by said input data signal is met by the scaling engine 25 (Fig. 3, col. 6, line 55 to col. 7, line 40 col. 7, lines 41-57).

In considering claim 6, the claimed wherein said processor is further arranged to convert said input data signal representing an image into a color-sequential output signal is met by the SLM 16 which generates an image for each of three different colors of a picture and these three images are sequentially displayed through a color wheel 17 (Fig. 1, col. 4, line 38 to col. 5, line 20).

In considering claim 7, the claimed wherein said processor is arranged to convert said input data signal representing an image into a sub-field modulated output signal for controlling the display unit is met by the pulse-width modulation techniques (col. 1, line 13 to col. 2, line 27).

In considering claim 8, the claimed wherein a part of the memory means is arranged as a cyclic memory for storing a part of an image represented by the input data signal is met by the memory 33 (Fig. 3, col. 6, lines 12-26 col. 7, lines 41-57).

In considering claim 9, the claimed wherein the memory manager has a further input for a second data input signal, and the memory manager is arranged to transfer the second data input signal to the memory means is met by the memory manager 22 which receives the control signals from the decoding engine 24 (Fig. 3, col. 5, lines 46-51, col. 6, lines 21-26 and col. 7, lines 41-57).

In considering claim 10, the claimed a digital video-processing unit having an output for providing the output data signal and a display system coupled to the output of said digital video-processing unit for displaying of said output data signal is met by the display optics 18 and the screen (Fig. 1).

***Conclusion***

4. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Trang U. Tran** whose telephone number is **(703) 305-0090**.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **John W. Miller**, can be reached at **(703) 305-4795**.

**Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

**or faxed to:**


**(703) 872-9314 (for Technology Center 2600 only)**

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Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

TT TT  
January 10, 2004

  
**MICHAEL H. LEE**  
**PRIMARY EXAMINER**